

J10000 U.S. PRO
10/04/1796
01/07/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10041796	01/07/2002	360	46	2651	<i>Figueroa</i>

**APPLICANTS: Cheung Wayne; Jove Stephen;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO IBM1P009/SJ0920010140US1
TITLE : CMOS low-noise MR read head pre-amplifier circuit			

U.S. DEPT. OF COMMERCE /PAT. & TM-PTO-436 (Rev. 12-34)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Dwg.	Print Fig.
		Primary Examiner		
TERMINAL DISCLAIMER		PREPARED FOR ISSUE Application Examiner		
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